**Appendix – VIII**

**(Schedule of Quantity)**

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| --- | --- | --- |
| **Sr. No.** | **Name of Item/Equipment** | **Qty.** |
| 8 | **DSP Floating Point Starter Kits**   * TMS320C6701 * C67x floating-point DSP- up to 167MHz, McBSP * DSP 1 C67x * DSP MHz (Max) 150,167 * CPU 32-/64-bit * Operating system DSP/BIOS * temperature range 0 to 90, -40 to 105 * 120-, 150-, 167-MHz Clock Rate * Eight 32-Bit Instructions/Cycle * 1 GFLOPS * Byte-Addressable (8-, 16-, 32-Bit Data) * 8-Bit Overflow Protection * 1M-Bit On-Chip SRAM * 512K-Bit Internal Program/Cach * (16K 32-Bit Instructions) * 512K-Bit Dual-Access Internal Data (64K Bytes) * 32-Bit External Memory * 52M-Byte Addressable External Memory Space * 16-Bit Host-Port Interface (HPI) * Direct Interface to T1/E1, | 04 |